IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

JAE-BON KOO

Serial No.:	10/764,525	Examiner:	MAIS, MARK A
Filed:	27 January 2004	Art Unit:	2616

For: FLAT PANEL DISPLAY WITH ANODE ELECTRODE LAYER AS POWER SUPPLY LAYER AND FABRICATION METHOD THEREOF

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O.Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F.R. §1.56, and §§1.97 and 1.98 as amended, Applicant cites, describes, and provides copies of the following art references. Under 37 C.F.R. §1.98(a)(2) however, copies of U.S. patent reference(s) are not provided.

FOREIGN PATENT REFERENCES:

- Japanese Patent Publication No. JP2001-134214 to Furumiya, et al., entitled *DISPLAY DEVICE* published on 18 May 2001(with English abstract).
- Japanese Patent Publication No. JP2001-332383 to Nozawa, et al., entitled *MANUFACTURING METHOD OF ORGANIC EL DISPLAY BODY, ARRANGEMENT METHOD OF SEMICONDUCTOR ELEMENT, MANUFACTURING METHOD OF SEMICONDUCTOR DEVICE*, published on 30 November 2001(with English abstract).

OTHER DOCUMENTS:

• Japanese Office action corresponding to Japanese Patent Application No. JP2003-354515, issued on 10 April 2007

DISCUSSION

According to the Japanese Office action issued on 10 April 2007, corresponding to Japanese Patent Application No. JP2003-354515, **Furumiya, et al., JP'214** discusses that this device is a display device in which display pixels 110 which are respectively provided with an EL element 60 having a light emitting layer between a cathode 67 and a anode 61, a TFT for switching 30 supplying a driving signal to the element 60 and a TFT for driving an EL element supplying a current to the EL element 60 are arranged in a matrix shape and which is provided with driving signal lines 52a, 52b supplying a driving signal to the TFT for switching 30 and driving power source lines 53a, 53b supplying a current corresponding to the driving signal to the EL element 60 and the driving signal lines 52a, 52b are arranged by being made to be away from the driving power source lines 53a, 53b in this device.

Nozawa, et al., JP'383 discusses that units of fine structure are numerously formed in parallel on a silicon wafer. This unit includes driving elements (a switching transistor 34, a driving transistor 37, a capacitor 36) of organic EL element (picture element) 35. This silicon wafer is divided to form a unit block 39. This unit block 39 is arranged at a predetermined position of a glass substrate (a substrate for display) 52. Driving elements for each picture element 35 are connected with a signal wire 31, a power supply wire 32, a scanning wire 33, a capacitor wire 38.
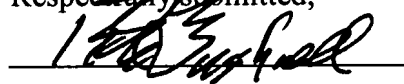
Pursuant to 37 CFR §1.97(d), the undersigned attorney hereby certifies that each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign patent application not more than three (3) months prior to the filing of the statement.

The citation of the foregoing references is not intended to constitute an assertion that other

or more relevant art does not exist. Accordingly, the Examiner is requested to make a wide-ranging and thorough search of the relevant art.

No fee is incurred by this Statement.

Respectfully submitted,

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INFORMATION DISCLOSURE STATEMENT		SERIAL NUMBER 10/764,525		DOCKET NO. P56937
PTO-1449 (PAGE 1 OF 1)		APPLICANT JAE-BON KOO		
		FILING DATE 27 January 2004		GROUP 2616

U.S. PATENT DOCUMENTS

EXAMINER	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE

FOREIGN PATENT DOCUMENTS | TRANSLATION

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	YES	NO
	JP2001-134214	5/18/01	Japan			Abstract	
	JP2001-332383	11/30/01	Japan			Abstract	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

	Japanese Office action corresponding to Japanese Patent Application No. JP2003-354515, issued on 10 April 2007.

EXAMINER:	DATE CONSIDERED:

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP §609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.